Refine Search

Search Results -

Terms	Documents
((virtual or logical) near5 port) same concentrator	31

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search: L3

Refine Search

Recall Text | Clear | Interrupt

Search History

DATE: Tuesday, January 10, 2006 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L3	((virtual or logical) near5 port) same concentrator	31	L3
L2	((virtual or logical) near5 port) same concentrator same bus	1	L2
L1	((virtual or logical) near5 port) same physical same concentrator same bus	1	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
((virtual or logical) near5 port) same concentrator	1

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L4

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Tuesday, January 10, 2006 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	((virtual or logical) near5 port) same concentrator	1	L4
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L3	((virtual or logical) near5 port) same concentrator	31	L3
L2	((virtual or logical) near5 port) same concentrator same bus	1	L2
L1	((virtual or logical) near5 port) same physical same concentrator same bus	1	L1

END OF SEARCH HISTORY

Refine Search

Search Results -

Terms	Documents
L1 and concentrator	0

Database:
US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search: L4

Refine Search

Recall Text Clear Interrupt

Search History

DATE: Tuesday, January 10, 2006 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR			
L4	L1 and concentrator	0	L4
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L3	L1 same concentrator	1	L3
L2	L1 and concentrator	36	L2
L1	((virtual or logical) near10 port) same bus	1197	L1

END OF SEARCH HISTORY

Freeform Search

Database: US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Term: L1 and concentrator

Display: 10 **Documents in Display Format:** - **Starting with Number** 1

Generate: Hit List Hit Count Side by Side Image

Search Clear Interrupt

Search History

DATE: Tuesday, January 10, 2006 Printable Copy Create Case

Set Name side by side	Query	Hit Count	Set Name result set
DB=PGPB,USPT,USOC; PLUR=YES; OP=OR			
L3	L1 same concentrator	1	L3
L2	L1 and concentrator	36	L2
L1	((virtual or logical) near10 port) same bus	1197	L1

END OF SEARCH HISTORY

Results for "((virtual port)<in>metadata) and bus"

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Your search matched 2 of 1293212 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

View Session History

New Search

Modify Search

((virtual port)<in>metadata) and bus

Check to search only within this results set

» Key

Display Format: Citation Citation & Abstract

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select Article Information

1. **A multiprocessor architecture for high-rate communication processing**
Johnson, E.E.;
Military Communications Conference, 1991. MILCOM '91, Conference Record, 'Military Communications in a Changing World'., IEEE
4-7 Nov. 1991 Page(s):1001 - 1006 vol.3
Digital Object Identifier 10.1109/MILCOM.1991.258421
AbstractPlus | Full Text: PDF(348 KB) IEEE CNF

2. **Evaluation of Futurebus+ for a GMMP multiprocessor**
Johnson, E.E.; Moore, R.S.; Polson, J.T.;
Computing and Information, 1992. Proceedings. ICCI '92., Fourth International Conference on
28-30 May 1992 Page(s):441 - 444
Digital Object Identifier 10.1109/ICCI.1992.227617
AbstractPlus | Full Text: PDF(340 KB) IEEE CNF

A multiprocessor architecture for high-rate communication processing

Johnson, E.E.
New Mexico State Univ., Las Cruces, NM, USA;

This paper appears in: **Military Communications Conference, 1991. MILCOM '91, Conference Record, 'Military Communications in a Changing World'., IEEE**
Publication Date: 4-7 Nov. 1991
On page(s): 1001 - 1006 vol.3
Meeting Date: 11/04/1991 - 11/07/1991
Location: McLean, VA
INSPEC Accession Number:4264065
Digital Object Identifier: 10.1109/MILCOM.1991.258421
Posted online: 2002-08-06 18:05:46.0

Abstract
The author presents a general-purpose multiprocessor architecture which accommodates an I/O bandwidth of many Gb/s through the use of VRAM in the main memory. The **virtual port** memory architecture is a global-memory-message-passing multiprocessor which is well suited to I/O-intensive real-time processing. This **bus**-based architecture permits incremental adjustments in I/O bandwidth, memory size, and processing power by simply adding or removing I/O controllers, memory modules, and processors. This architecture is described, followed by an analysis of its performance in handling various communication processing tasks, including the 4×300 Mb/s data stream at the NASA Tracking and Data Relay Satellite System (TDRSS) ground terminal

Index Terms
Inspec

Controlled Indexing
computer networks data communication systems multiprocessing systems parallel architectures satellite relay systems telecommunications computing

Non-controlled Indexing
NASA Tracking and Data Relay Satellite System VRAM **bus**-based architecture data communication global-memory-message-passing multiprocessor high-rate communication processing multiprocessor architecture performance **virtual port** memory architecture

Author Keywords
Not Available

References

No references available on IEEE Xplore.

Citing Documents

No citing documents available on IEEE Xplore.

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Results for "((virtual port)<in>metadata) and concentrator"

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» Search Options

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New Search

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((virtual port)<in>metadata) and concentrator

Check to search only within this results set

Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

No results were found.

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